



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

in the Application of:

Gary L. Swoboda, et al. TI-30481
Serial No: 09/943,599 Art Unit: 2123
Filed: August 30, 2001 Examiner: _____
For: Correlating On-Chip Data Processor Trace Information for Export Conf. No.: 2479

INFORMATION DISCLOSURE STATEMENT

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)

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Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. Copies of the noted references are enclosed herewith.

Since this IDS is being submitted before any Office Action, no fee is due.

Respectfully submitted,



Robert D. Marshall, Jr.
Attorney for Applicant(s)
Reg. No. 28,527

Texas Instruments Incorporated
P.O. Box 655474, MS 3999
Dallas, TX 75265
(972) 917-5290

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STATEMENT BY APPLICANT

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Complete If Known

Application Number	
Filing Date	August 30, 2001
First Named Inventor	Gary L. Swoboda, et al.
Group Art Unit	2123
Examiner Name	

Sheet 1 of 1 Attorney Docket No. TI-30481

U.S. PATENT DOCUMENTS

Exam. Initials*	Cite No.1	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

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CA	ARM Limited, RDI 1.5.1tx and RDI 1.5.1rt; Doc. No. RDI-0032-CUST-ESPC-A; 19 May, 2000; pgs. 1-55.		
CB	ARM Limited, ETM9, Rev. 1, Technical Reference Manual, Doc. No. DDI 0157C, pgs. i – Index-3.		
CC	ARM Limited, Embedded Trace Macrocell, Rev. 1, Specification, Doc. No. IHI 0014E, pgs. i – Index-3.		
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